



# CLARE

# ITC117PL

## Integrated Telecom Circuits



	ITC117PL	Units
Relay Blocking Voltage	350	V
Relay Load Current	120	mA
Relay Max R <sub>ON</sub>	20	Ω
Bridge Rectifier Reverse Voltage	100	V
Darlington Collector Current	120	mA
Darlington Current Gain	10,000	-

### Features

- Small 16 Pin SOIC Package (PCMCIA Compatible)
- Board Space and Cost Savings
- 2mW Hookswitch Drive Power (Logic Compatible)
- No Moving Parts
- 3750V<sub>RMS</sub> Input/Output Isolation
- Full-Wave Bridge Rectifier
- Darlington Transistor for Electronic Inductor "Dry" Circuits
- Full Wave Current Detector for Ring Signal or Loop Current Detect
- Current Limiting
- JEDEC Standard Lead Configuration

### Applications

- Data/Fax Modem
- Voice Mail Systems
- Telephone Sets
- Computer Telephony Integration
- Set Top Box Modems

### Description

The Integrated Telecom Circuit combines a 1-Form-A solid state relay, bridge rectifier, Darlington transistor and optocoupler into one 16 pin SOIC package, consolidating designs and reducing component count in telecom applications. The relay features the added benefit of current limiting. The ITC117's optocoupler provides for full wave detection of ring signals.

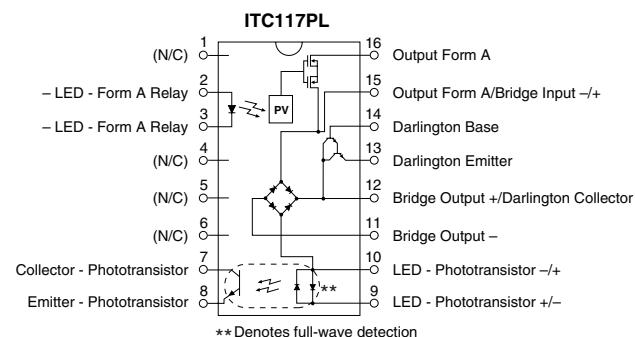
### Approvals

- UL Recognized: File Number E76270
- CSA Certified: File Number LR 43639-12
- Certified to:
  - EN 60950
  - EN 41003
  - IEC 950
  - AS/NZS 3260

### Ordering Information

Part #	Description
ITC117PL	16 Pin SOIC (50/Tube)
ITC117PLTR	16 Pin SOIC (1000/Reel)

### Pin Configuration



**Absolute Maximum Ratings (@ 25° C)**

Parameter	Min	Typ	Max	Units
Total Package Dissipation	-	-	1 <sup>1</sup>	W
Isolation Voltage				
Input to Output	3750	-	-	V <sub>RMS</sub>
Relay Blocking Voltage	-	-	350	V
Operational Temperature	-40	-	+85	°C
Storage Temperature	-40	-	+125	°C
Soldering Temperature	-	-	+220	°C

(10 Seconds Max.)

<sup>1</sup> Above 25° derate linearity 8.33mW/°C

Total Power Dissipation (PD):

$$P_D = P_{HOOKSWITCH} + P_{BRIDGE} + P_{DARLINGTON} + P_{LED}$$

$$P_D = (R_{DS(on)}) (I_L^2) + 2(V_F)(I_L) + (V_{CE})(I_L) + (V_{LED})(I_F)$$

WHERE:

 $R_{DS(on)}$  = Maximum realy on resistance $I_L$  = Maximum loop current $V_F$  = Maximum diode forward voltage $V_{CE}$  = Maximum voltage collector to emitter $V_{LED}$  = Maximum LED forward voltage $I_F$  = Maximum LED current

*Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this data sheet is not implied. Exposure of the device to the absolute maximum ratings for an extended period may degrade the device and effect its reliability.*

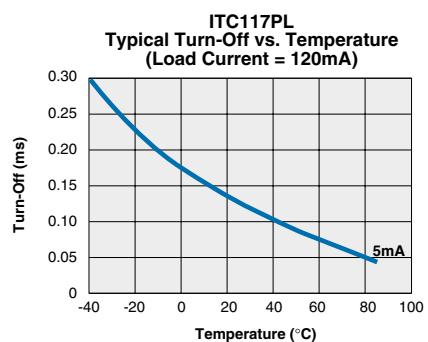
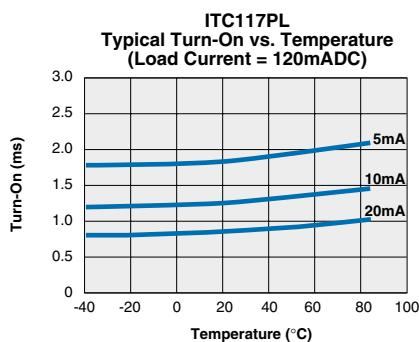
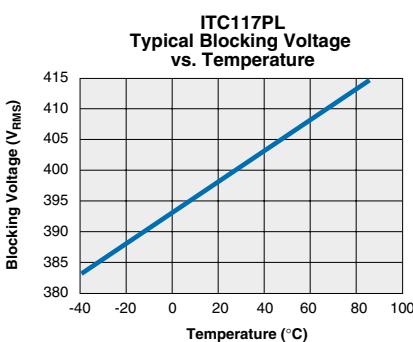
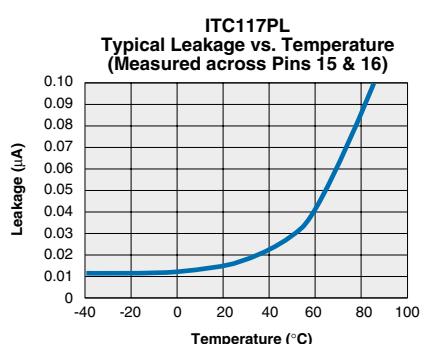
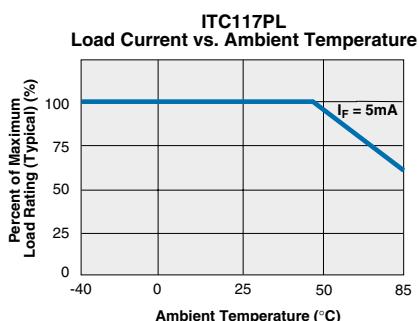
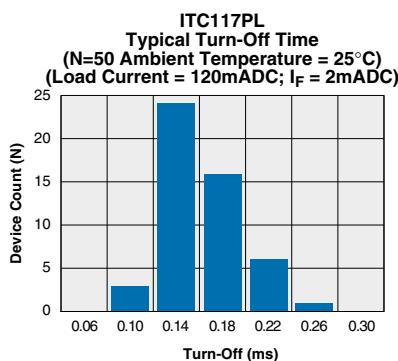
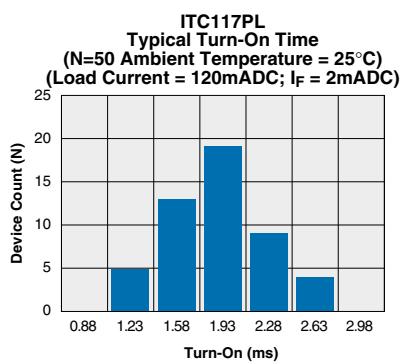
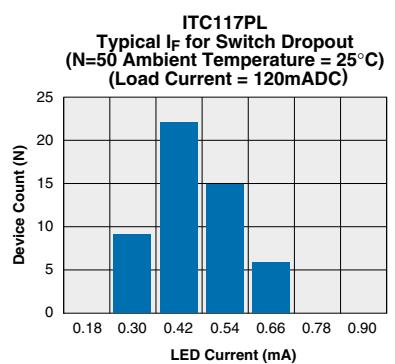
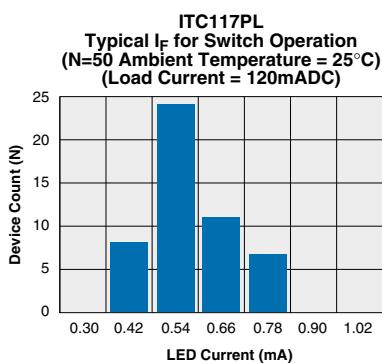
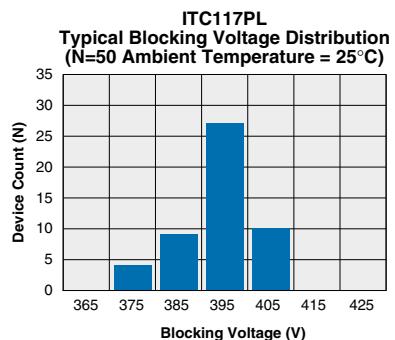
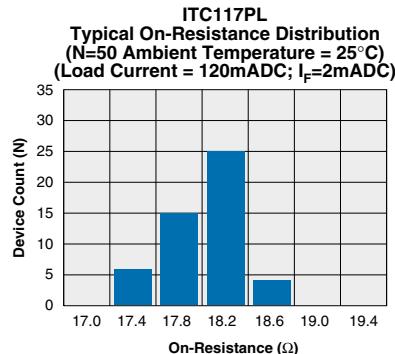
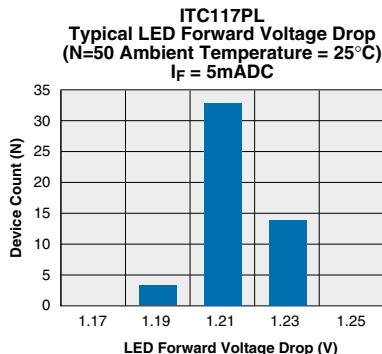
**Electrical Characteristics**

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Relay Portion (Pins 15,16)</b>						
<b>Output Characteristics @ 25°C</b>						
Load Current (Continuous)	-	I <sub>L</sub>	-	-	120	mA
On-Resistance	I <sub>L</sub> =120mA	R <sub>ON</sub>	-	-	20	Ω
Off-State Leakage Current	V <sub>L</sub> =350, T <sub>J</sub> =25°C	I <sub>LEAK</sub>	-	-	1	μA
Switching Speeds						
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	T <sub>ON</sub>	-	-	5	ms
Turn-Off	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	T <sub>OFF</sub>	-	-	3	ms
Output Capacitance	50V, f=1MHz	C <sub>OUT</sub>	-	25	-	pF
Current Limit	I <sub>F</sub> =5mA	I <sub>CL</sub>	190	235	280	mA
<b>Relay Portion (Pins 2,3)</b>						
<b>Input Characteristics @ 25°C</b>						
Input Control Current	I <sub>L</sub> =120mA	I <sub>F</sub>	5	-	50	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Voltage	-	V <sub>R</sub>	-	-	5	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Detector Portion (Pins 7,8)</b>						
<b>Output Characteristics @ 25°C</b>						
Phototransistor Blocking Voltage	I <sub>C</sub> =10μA	BV <sub>CEO</sub>	20	50	-	V
Phototransistor Dark Current	V <sub>CE</sub> =5V, I <sub>F</sub> =0mA	I <sub>CEO</sub>	-	50	500	A
Saturation Voltage	I <sub>C</sub> =2mA, I <sub>F</sub> =16mA	V <sub>SAT</sub>	-	0.3	0.5	V
Current Transfer Ratio	I <sub>F</sub> =6mA, V <sub>CE</sub> =0.5V	CTR	33	400	-	%
<b>Detector Portion (Pins 9,10)</b>						
<b>Input Characteristics @ 25°C</b>						
Input Control Current	I <sub>C</sub> =2mA, V <sub>CE</sub> =0.5V	I <sub>F</sub>	6	2	100	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Input Current (Detector must be off)	I <sub>C</sub> =1μA, V <sub>CE</sub> =5V	I <sub>F</sub>	5	25	-	μA

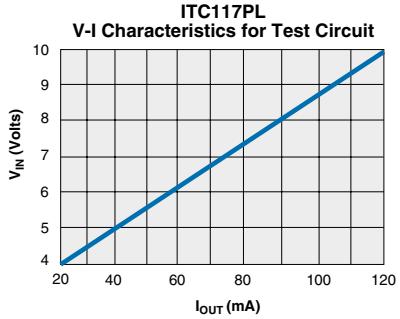
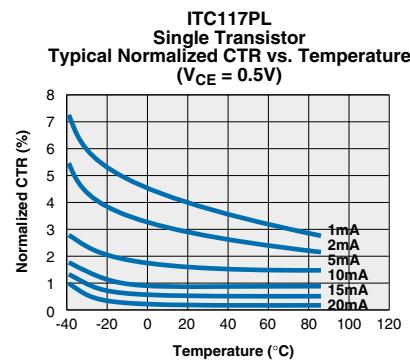
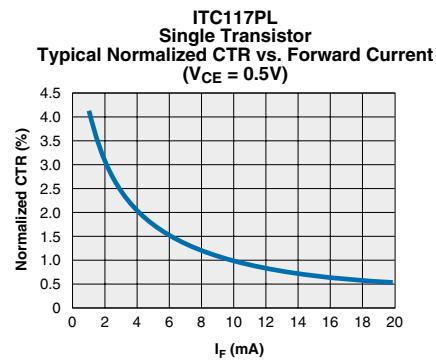
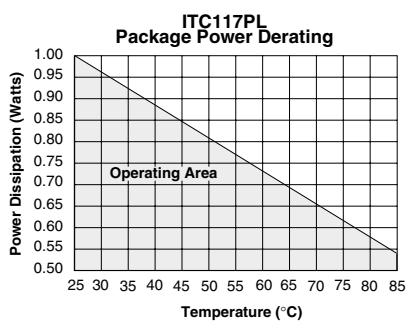
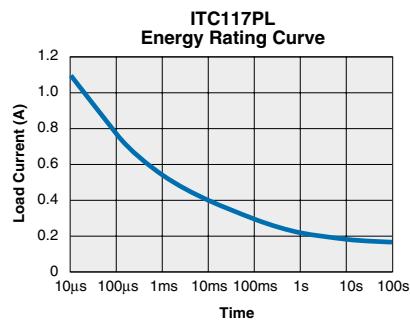
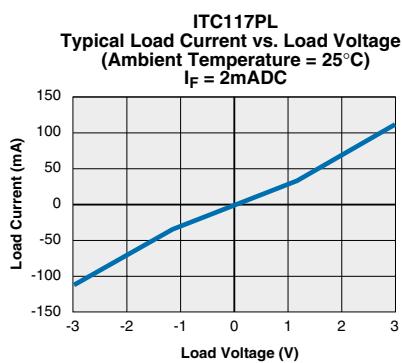
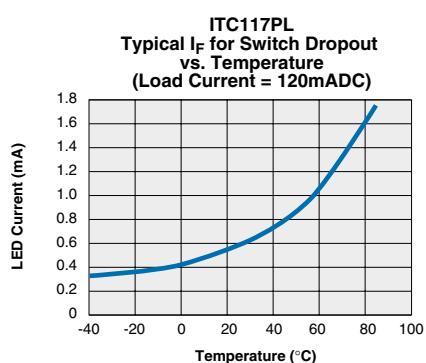
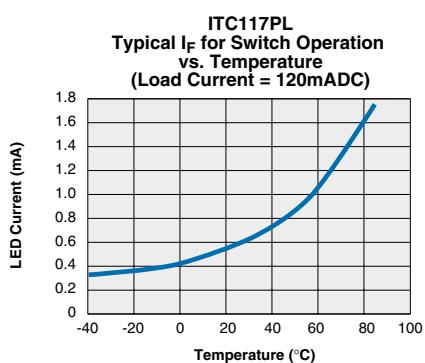
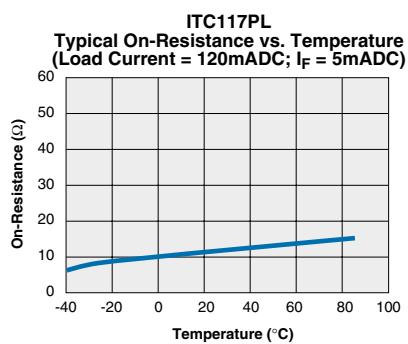
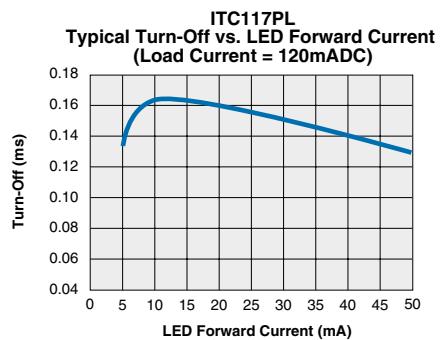
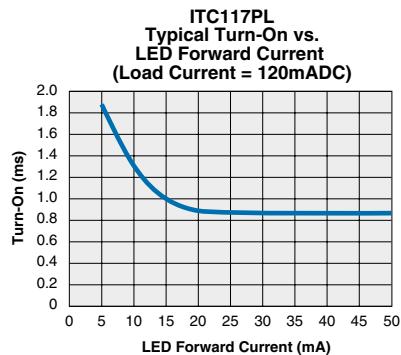
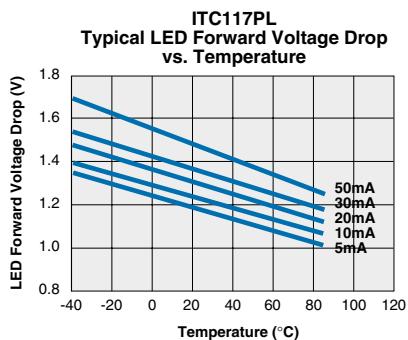
**Electrical Characteristics**

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Bridge Rectifier Electrical Ratings @ 25°C</b>						
Reverse Voltage	-	$V_{RD}$	-	-	100	V
Forward Drop Voltage	$I_{FD}=120\text{mA}$	$V_{FD}$	-	-	1.5	V
Reverse Leakage Current $T_J=85^\circ\text{C}$	$T_J=25^\circ\text{C}, V_R=100\text{V}$	$I_{RD}$ -	-	50	10	$\mu\text{A}$
Forward Current (Continuous)		$I_{FD}$	-	-	140	mA
Forward Current (Peak)	$t=10\text{mS}$	$I_{FD}$	-	-	0.5	A
<b>Darlington Electrical Ratings @ 25°C</b>						
Collector-Emitter Voltage	$I_c=10\text{mA DC}, I_B=0$	$V_{CEO}$	40	-	-	V
Collector-Current Continuous	$V_C=3.5\text{V}$	$I_c$	-	-	120	mA
Power Dissipation @ 25°C	-	$P_d$	-	-	500	mW
Off-State Collector Emitter Leakage Current	$V_{CE}=10\text{V}; I_B=0\text{mA}$	$I_{CEX}$	-	-	1	$\mu\text{A}$
DC Current Gain $V_{CE}=10\text{VDC}$	$I_c=120\text{mA},$	$h_{FE}$	10,000	-	-	
Saturation Voltage	$I_c=120\text{mA}$	$V_{CE(SAT)}$	-	-	1.5	V
Total Harmonic Distortion $I_c=40\text{mA}$	$f_0=300\text{Hz} @ -10\text{dBm}$	-	-	-	-80	dB

## PERFORMANCE DATA\*

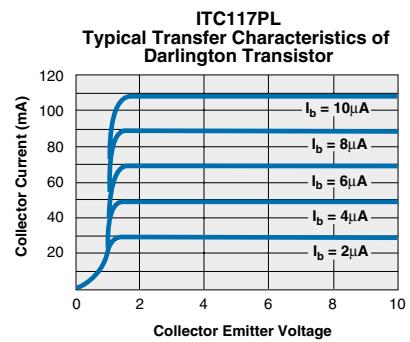
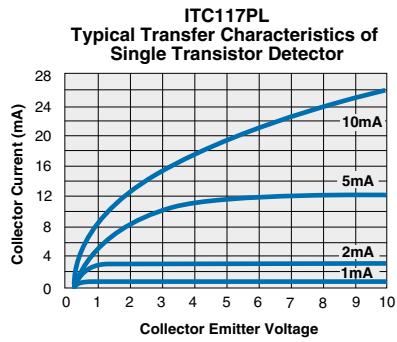
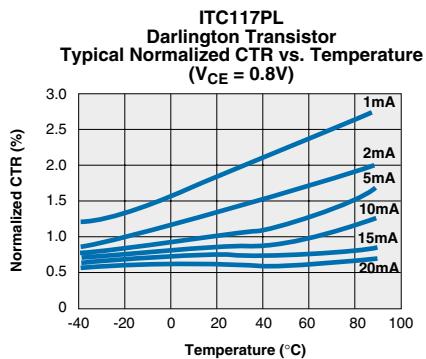
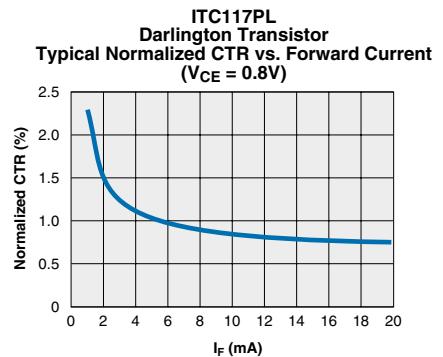
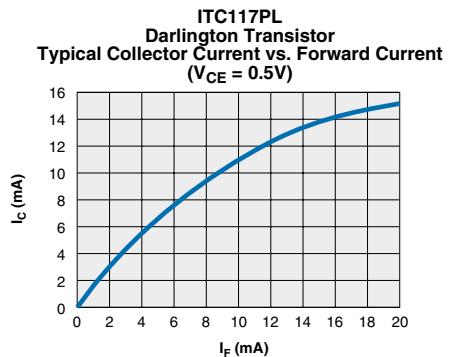
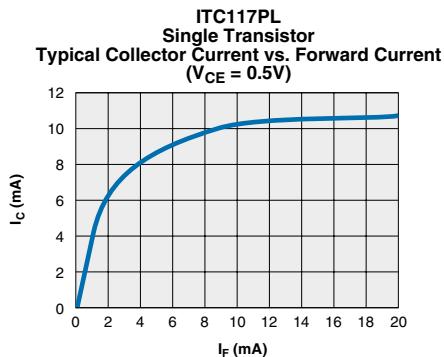


\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

**PERFORMANCE DATA\***


\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

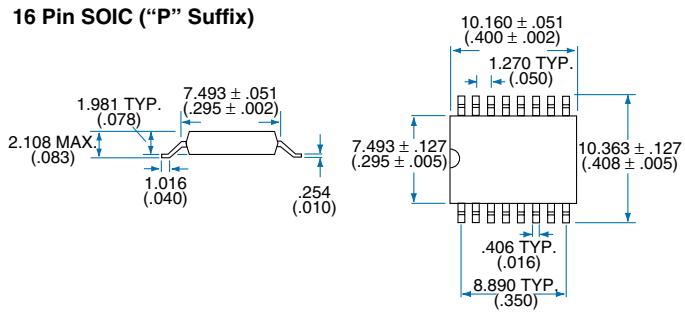
## PERFORMANCE DATA\*



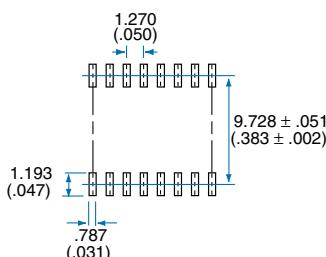
\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## MECHANICAL DIMENSIONS

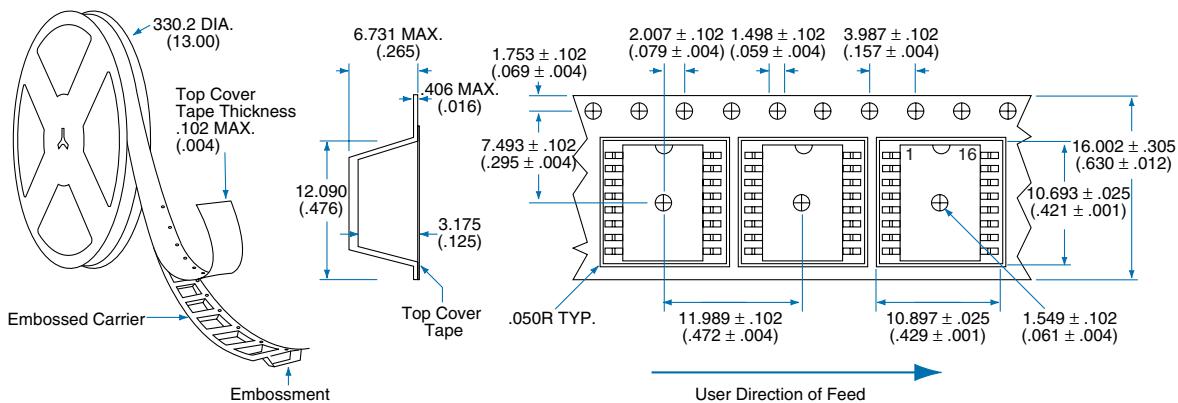
**16 Pin SOIC ("P" Suffix)**



**PC Board Pattern  
(Top View)**



**Tape and Reel Packaging for 16 Pin SOIC Package**



Dimensions  
mm  
(inches)



CLARE

---

**For additional information please visit our website at: [www.clare.com](http://www.clare.com)**

Clare, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in Clare's Standard Terms and Conditions of Sale, Clare, Inc. assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of Clare's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. Clare, Inc. reserves the right to discontinue or make changes to its products at any time without notice.